SAK-TC275TP-64F200N DC

Parametrics

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Support

Parametric	SAK-TC275TP-64F200N DC
Clock Frequency **max**	200.0MHz
SRAM (incl. Cache)	472.0kByte
CAN Nodes	4
A/D Input Lines	48
Program Memory	4.0MByte
Type of Memory	Flash
FlexRay™	yes
Number of ADC Modules	14.0
EEPROM Emulation	yes
Instruction Width ([bits])	32/16
Instruction Set Architecture	TriCore™ v1.6E (32-bit)
Instruction Set Architecture	TriCore™ v1.6P (32-bit)
DMA Channels	64
External Bus Interface	no
Fast Flash Programming	no
Temperature	-40°C – +125°C
DSP Functionality	yes
On-chip Clock Generation	yes
I/O Operation Voltages	3.3V
I/O Operation Voltages	5.0V
Touch/LED Matrix Control	no
On-chip Voltage Regulator	yes
Real Time Clock	yes
Floating Point Unit	yes
Watchdog Timer	yes
Oscillator Watchdog	yes

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